

## 3800 MAIN PWA

22-Jul-97

### LEDS

In normal operation, +5V & Load LEDs should be on, Stop LED will cycle on & off.

If the reset or halt LEDs (both red) are on the CPU is dead.

Load LED indicates that the FPGA are loaded.

Diag. 1-4 LED's are software dependent.

Dip switch, S1, normal 1 on rest off.

J11 is used by R & E as a serial port.

U40 is a future, to be used with thermocouples.

U43 is a future, for an additional MUX.

Have test points for +-24V, +-15V, +-5 V, and +5.25V.

The eyelet above the rear detector connector is the only DC ground to chassis, others are EMI grounds.